

Q-4	Attempt all questions	(14)
	(a) Explain in brief different special I/O modules	5
	(b) Explain in brief with diagram PLC scanning process considerations.	5
	(c) Explain in detail different PLC disadvantages.	4
Q-5	Attempt all questions	(14)
	(a) Draw the ladder logic programs for basic logic gates. Express each of the following equations as a ladder logic program: 1. $Y = AB + CDE + \bar{A}\bar{B}$ 2. $Y = (A+BC)CD$ 3. $Y = \bar{A}\bar{B}C + \bar{D} + E$	7
	(b) Explain in detail memory organization in PLC (Hint. Program and Data files).	7
Q-6	Attempt all questions	(14)
	(a) Explain in detail with examples different types of branching used in PLC ladder program.	7
	(b) List the five standard PLC languages as defined by the International Standard for PLCs, and give a detail description of each with examples.	7
Q-7	Attempt all questions	(14)
	(a) Enlist different timer instructions available in PLC. Explain any three of them in detail.	7
	(b) Enlist different counter instructions available in PLC. Explain each of them in detail.	7
Q-8	Attempt all questions	(14)
	(a) Enlist the different data compare instructions available in PLC. Explain any three of them in detail with examples.	7
	(b) Enlist the different math instructions available in PLC. Explain any three of them in detail with examples.	7

